



Date : 07/07/2013

NOTICE

All the students of SE /TE/BE are hereby informed that Bridge course is scheduled from 10/07/2013 to 16/07/ 2013 as per the time table displayed on notice board . Follow the given schedule and be present for the same . Attendance is compulsory.

Mrs Priti Tyagi

(HOD ELECTRONICS)

PADMABHUSHAN VASANTDADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION (E) - MUMBAI-22
BRIDGE TIME TABLE

BE
SH-13

TITLE : ACADEMIC SCHEDULE (SECOND HALF - 2013)
DEPT : ELECTRONICS ENGINEERING
W.E.F. : 10/07/2013 to 16/07/13
Sem - BE (SEM VII)
CLASSROOM NO-321

TIME(HRS) DAYS	9:00 TO 10:00	10:00 TO 11:00	11:00 TO 12:00	12:00 TO 13:00	13:00 TO 13:30 TO 13:30	13:30 TO 14:30	14:30 TO 15:30	15:30 TO 16:30	16:30 TO 17:30
MONDAY	PE&D	VLSI	IP	CN	L	FD	IP	PE&D	PE&D
TUESDAY	CN	PE&D	IP	VLSI	U	FD	PE&D	VLSI	VLSI
WEDNESDAY	IP	PE&D	FD	VLSI	N	CN	CN	IP	IP
THURSDAY	IP	FD	VLSI	CN	C	FD	FD	IP	IP
FRIDAY	CN	PE&D	VLSI	VLSI	H	PE&D	FD	CN	CN

SUB	CLASS TEACHER	THEORY	TOTAL
VLSI Design		08	08
Digital Image Processing Design	Mrs Priti Tyagi	08	08
Filter Design	Mr Promod Bhavarte	08	08
Power Electronics And Drives	Mr Hemant Jadhav	08	08
Communication Networks	Mrs Neha Mahajan	08	08
			40

MRS. NILIMA ZADE, MRS. PRIYA GUPTA
(Time Table Committee Member)

Mrs. MEPHA KULKARNI
(Time Table Committee Convener)

Mrs. PRITI TYAGI
(HOD ELEX)

Dr. K. T. V REDDY
(PRINCIPAL)

PADMABHUSHAN VASANTDADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION (E) -MUMBAI-22
BRIDGE TIME TABLE

TE - SH-13

TITLE : ACADEMIC SCHEDULE (SECOND HALF - 2013)

DEPT : ELECTRONICS ENGINEERING

W.E.F. : 10/07/2013 to 16/07/13

CLASS - TE (SEM V)


CLASSROOM NO-320

TIME(HRS) DAYS	9:00 TO 10:00	10:00 TO 11:00	11:00 TO 12:00	12:00 TO 13:00	13:00 TO 13:30	13:30 TO 14:30	14:30 TO 15:30	15:30 TO 16:30	16:30 TO 17:30
MONDAY	EME	LICD	EVS	MPMC-I	L	DCCT	DCCT	CTSS	EME
TUESDAY	CTSS	CTSS	DCCT	LICD	U	EME	EVS	MPMC-I	MPMC-I
WEDNESDAY	MPMC-I	DCCT	EME	CTSS	N	MPMC-I	EVS	LICD	LICD
THURSDAY	EVS	DCCT	LICD	LICD	C	CTSS	EME	MPMC-I	CTSS
FRIDAY	DCCT	DCCT	EME	EVS	H	LICD	MPMC-I	CTSS	EME

SUB	CLASS TEACHER	THEORY	TOTAL
Microprocessor and Microcontroller I	Mrs Priya Gupta	07	07
Linear Integrated Circuits & Design	Mr Ramakant Patil	07	07
Electromagnetic Engineering	Mrs Neha Mahajan	07	07
Continuous Time Sigls and Systems	Mrs Radha Wanode	07	07
Digital Communication & Coding techniques	Mrs Leena Govekar	07	07
Environmental Science	Mrs Priya Ozarde	05	05


MRS. NILIMA ZADE, MRS. PRIYA GUPTA
(Time Table Committee Member)


Mrs MEDHA KULKARNI
(Time Table Committee Convener)


Mrs. PRITI TYAGI
(HOD ELEX)


Dr. K. T. V. REDDY
(PRINCIPAL)

PADMABHUSHAN VASANTDADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION (E) –MUMBAI-22
BRIDGE TIME TABLE

SE - SH.13

TITLE : ACADEMIC SCHEDULE (SECOND HALF - 2013)
DEPT : ELECTRONICS ENGINEERING
W.E.F. : 10/07/2013 to 16/07/13
CLASS - SE (SEM III)
CLASSROOM NO-319

TIME(HRS) DAYS	9:00 TO 10:00	10:00 TO 11:00	11:00 TO 12:00	12:00 TO 13:00	13:00 TO 13:30	13:30 TO 14:30	14:30 TO 15:30	15:30 TO 16:30	16:30 TO 17:30	
MONDAY	ED	AM III	EIM	DCD	L	CT	CT	EIM	DCD	
TUESDAY	AM III	ED	ED	CT	U	EIM	EIM	DCD	DCD	
WEDNESDAY	CT	AM III	AM III	EIM	N	ED	ED	DCD	CT	
THURSDAY	EIM	CT	ED	ED	C	AM III	AM III	DCD	DCD	
FRIDAY	AM III	AM III	CT	DCD	H	CT	ED	EIM	EIM	
SUB	CLASS TEACHER					THEORY				TOTAL
Applied Mathematics III	MRS AARTI KURKURE					08				08
Electronic Devices	MR.ROHAN MANDHARE					08				08
Digital Circuits and Design	MRS LEENA GOVEKAR					08				08
Circuit Theory	MRS RADHA WANODE					08				08
Electronic Instruments and Measurements	MR RAMAKANT PATIL					08				08
										40

Pritya
MRS. NILIMA ZADE, MRS. PRIYA GUPTA
(Time Table Committee Member)

Medha
Mrs MEDHA KULKARNI
(Time Table Committee Convener)

Pritya
MRS. PRITYA AGI
(HOD ELEX)

Coor
Dr. K. T. V. REDDY
(PRINCIPAL)